

6502 SBC Bus

	Pin	Func	Read	Write	Dis.
43256 SRAM	20	CS	L	L	H
	22	OE	L	X	X
	27	WE	H	L	X

15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
L	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
32 KiB												\$0000-\$7FFF			

	Pin	Func	Read	Write	Dis.
6551 ACIA	2	CS0	H	H	L
	3	CS1	L	L	H
	28	R/W	H	L	X

15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
H	L	X	X	L	X	X	X	X	X	X	X	X	X	X	X
2 KiB												\$8000-\$87FF			

	Pin	Func	Read	Write	Dis.
6522 VIA	40	CS1	H	H	L
	39	CS2	L	L	H
	22	R/W	H	L	X

15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
H	L	X	X	H	X	X	X	X	X	X	X	X	X	X	X
2 KiB												\$8800-\$8FFF			

Reserved area for IO expansion on future SBC revisions.

\$9000-\$BFFF

	Pin	Func	Read	Write	Dis.
27C512 UV EPROM	20	E	L	N/A	H
	22	GVpp	L	N/A	X

15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
H	H	X	X	X	X	X	X	X	X	X	X	X	X	X	X
16 KiB												\$C000-\$FFFF			

Decoding logic and memory map for the Loom Communications 6502 SBC, Version 1.01 11/21/2008